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FIG.1

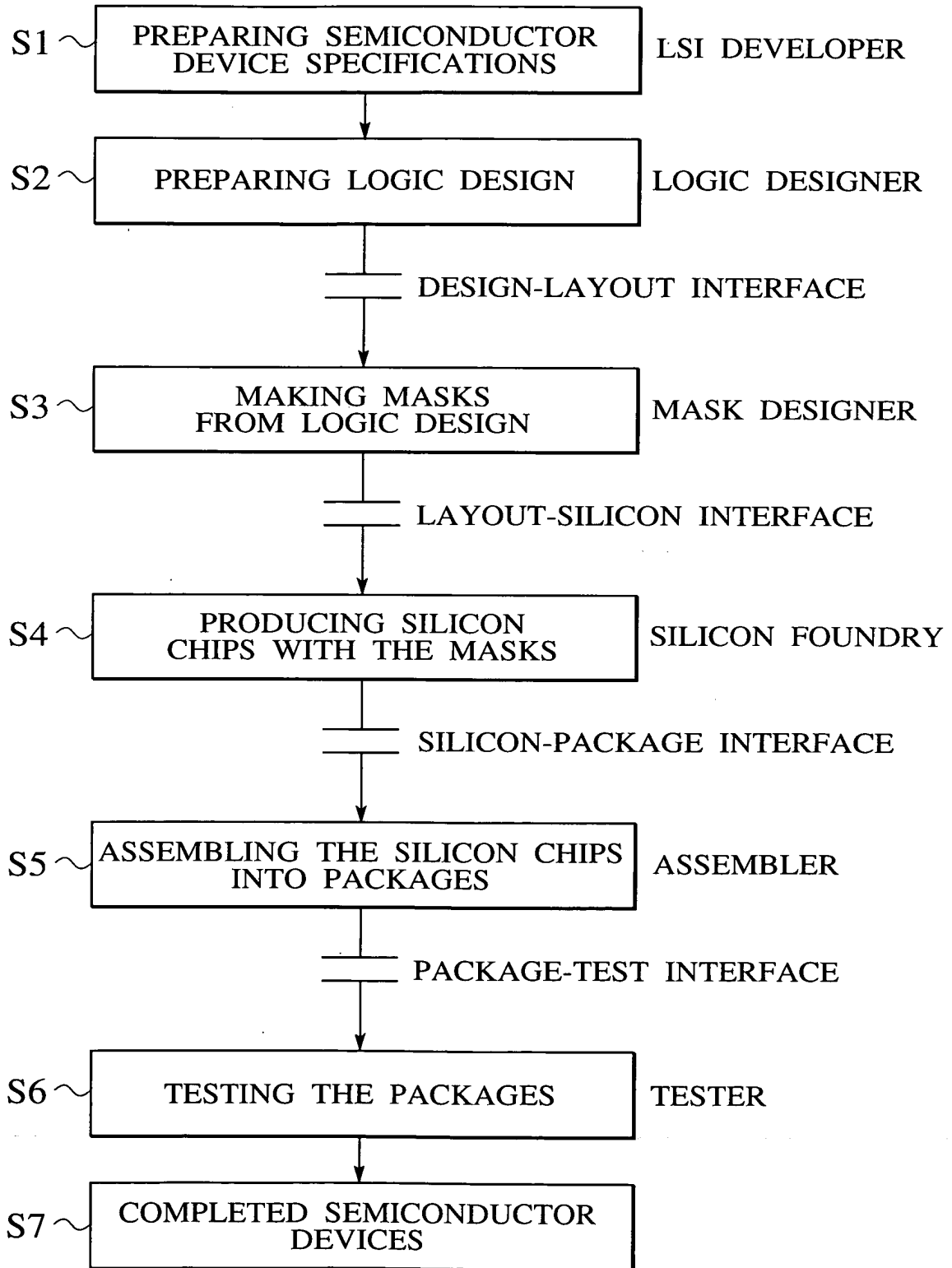
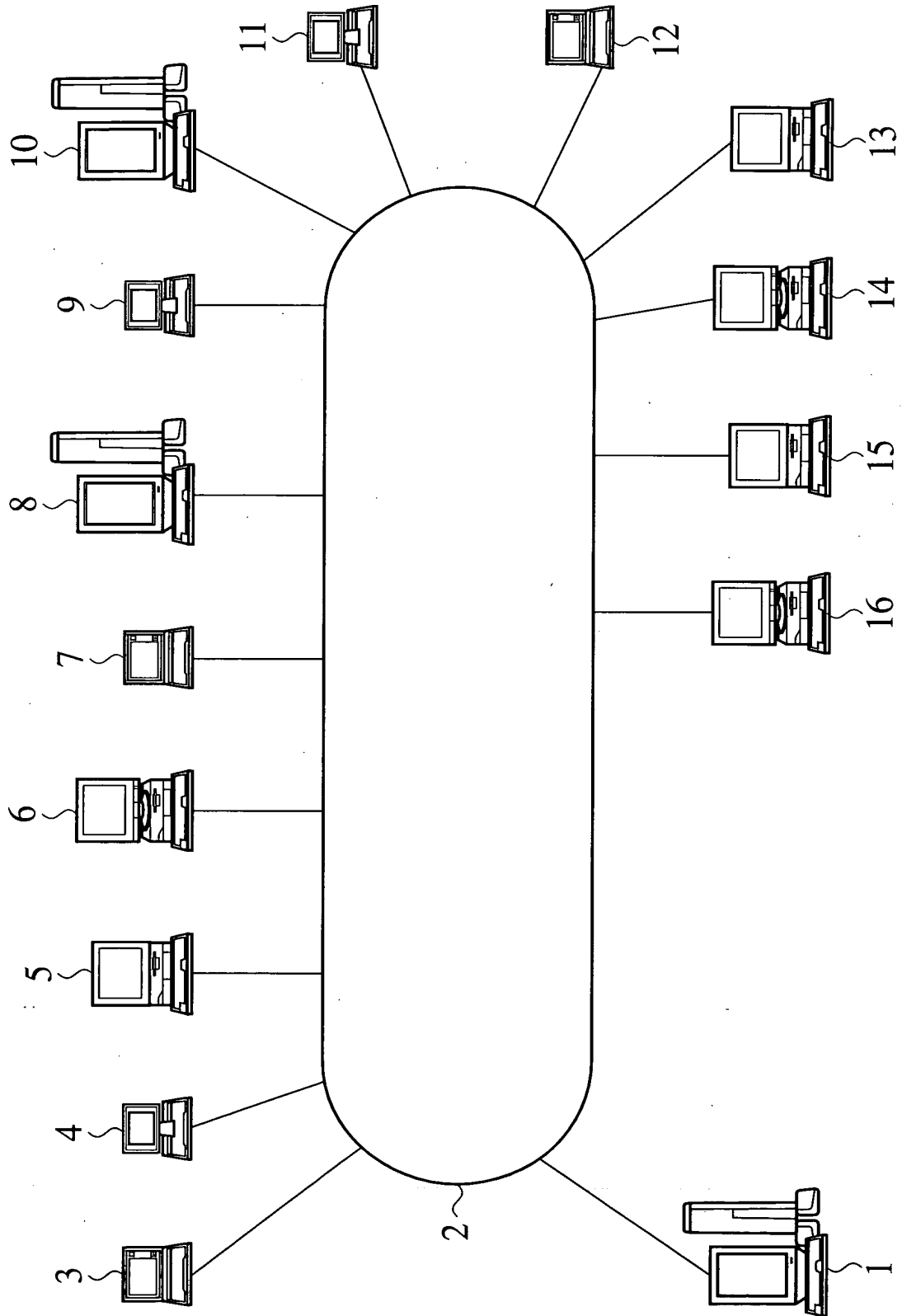
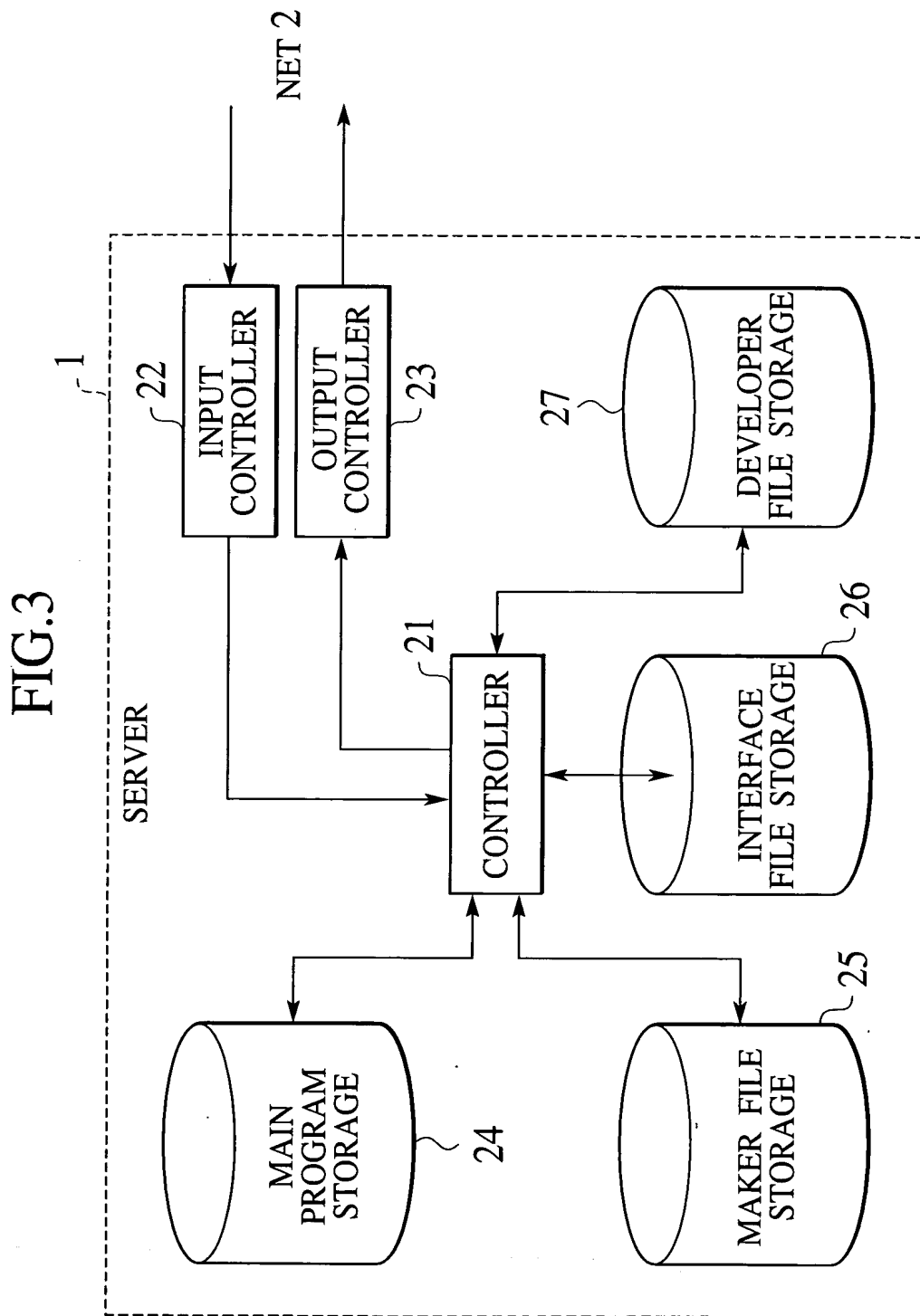


FIG.2



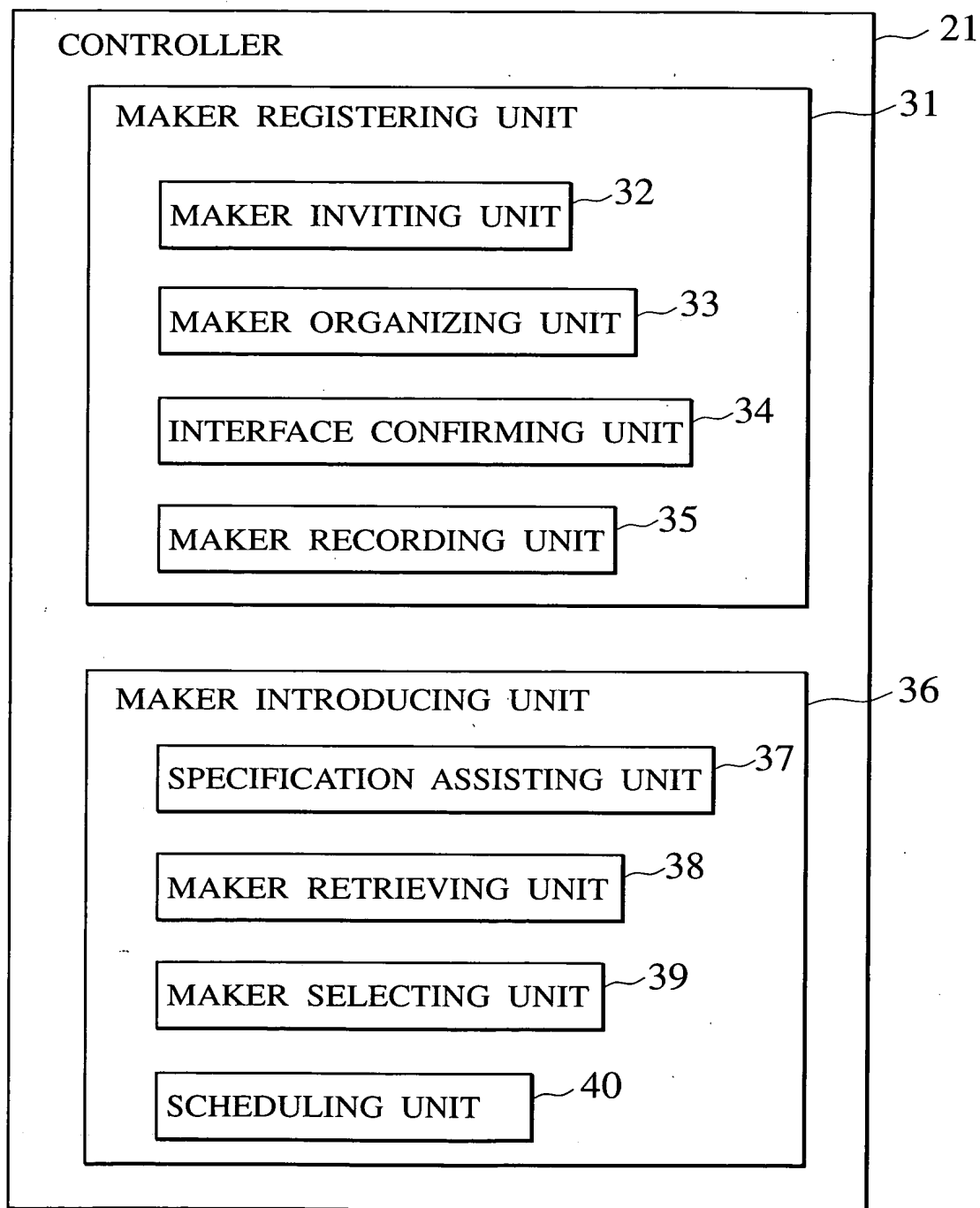
705200-2932680

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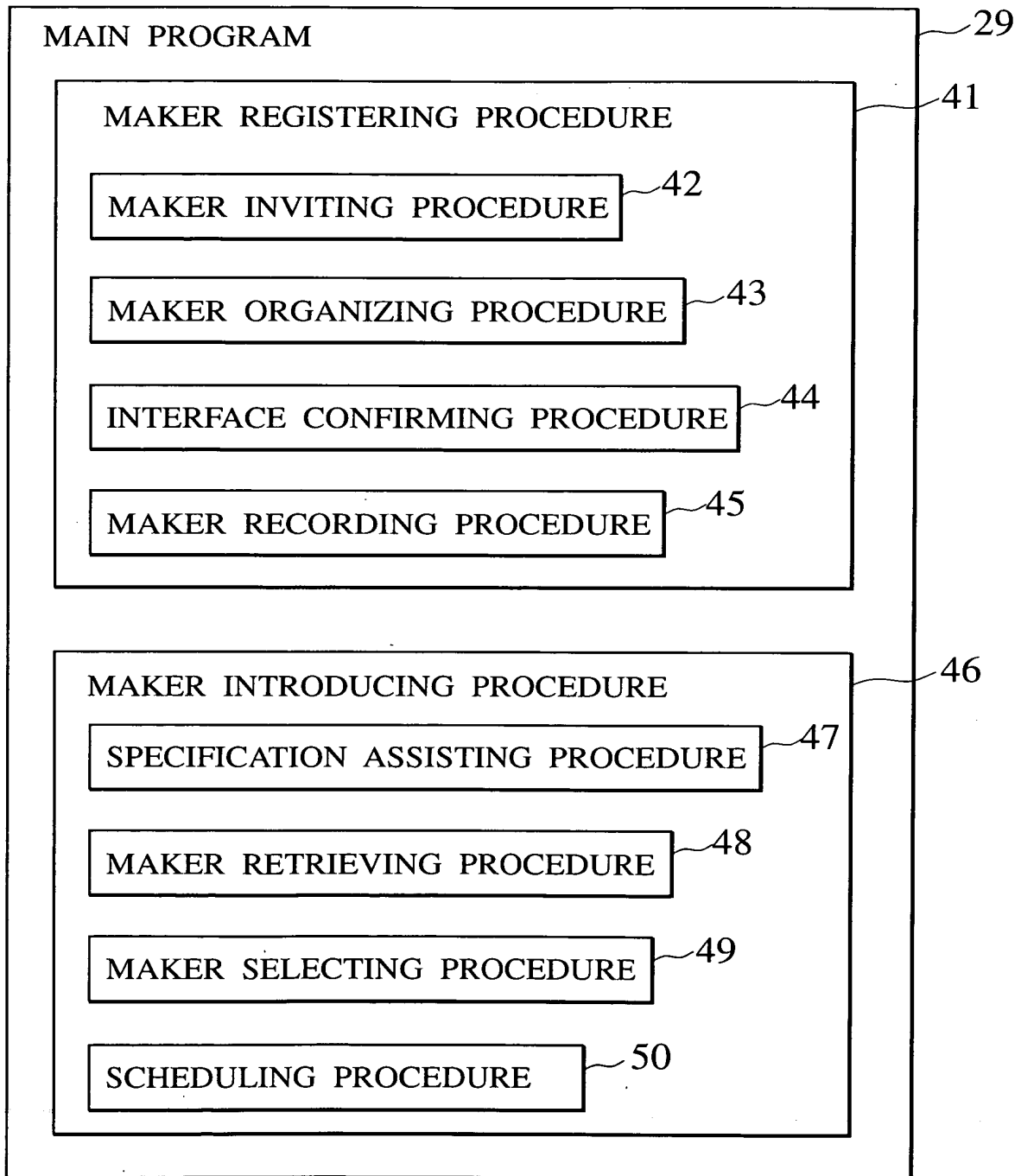
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FIG.4



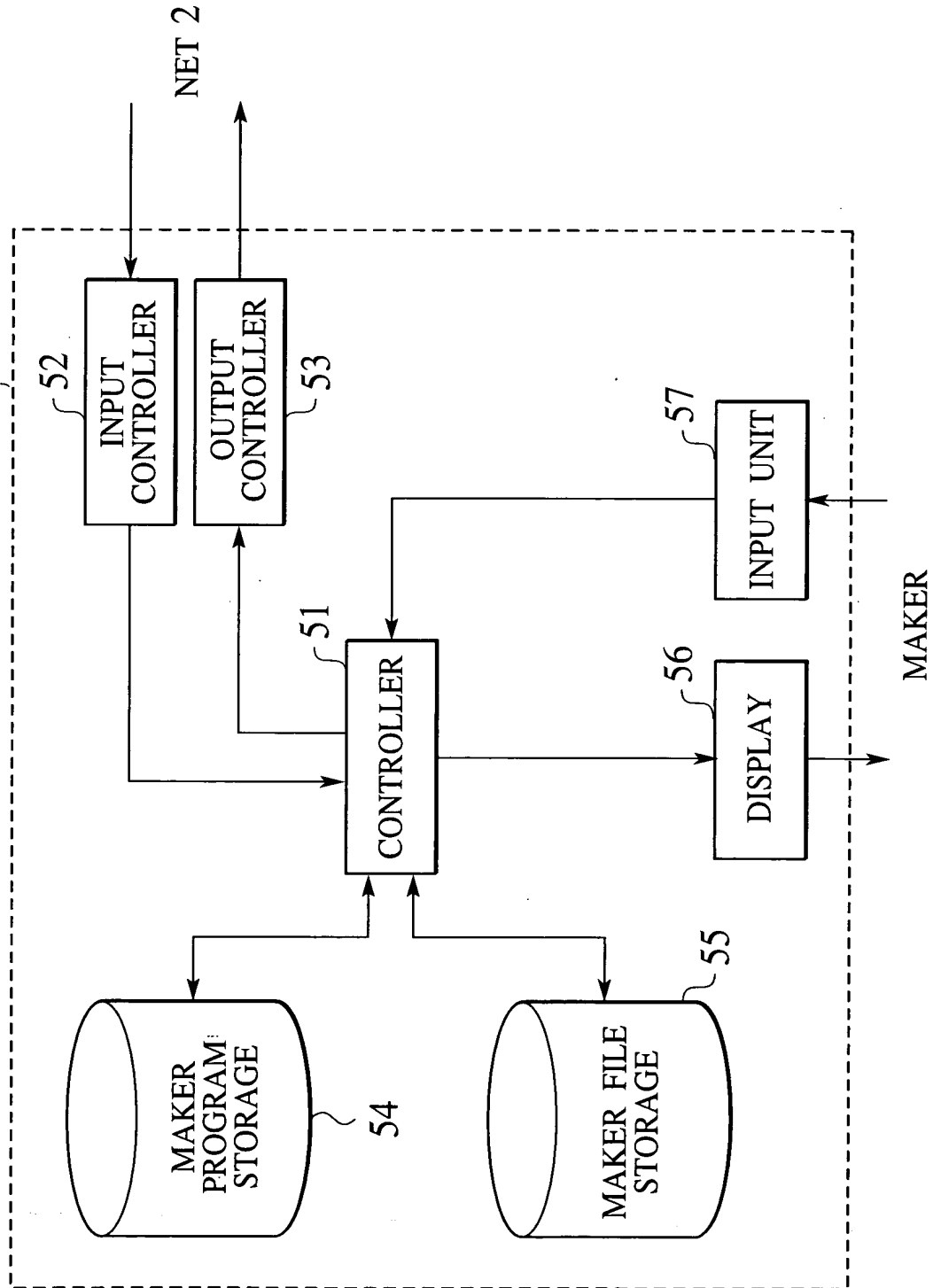
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FIG.5



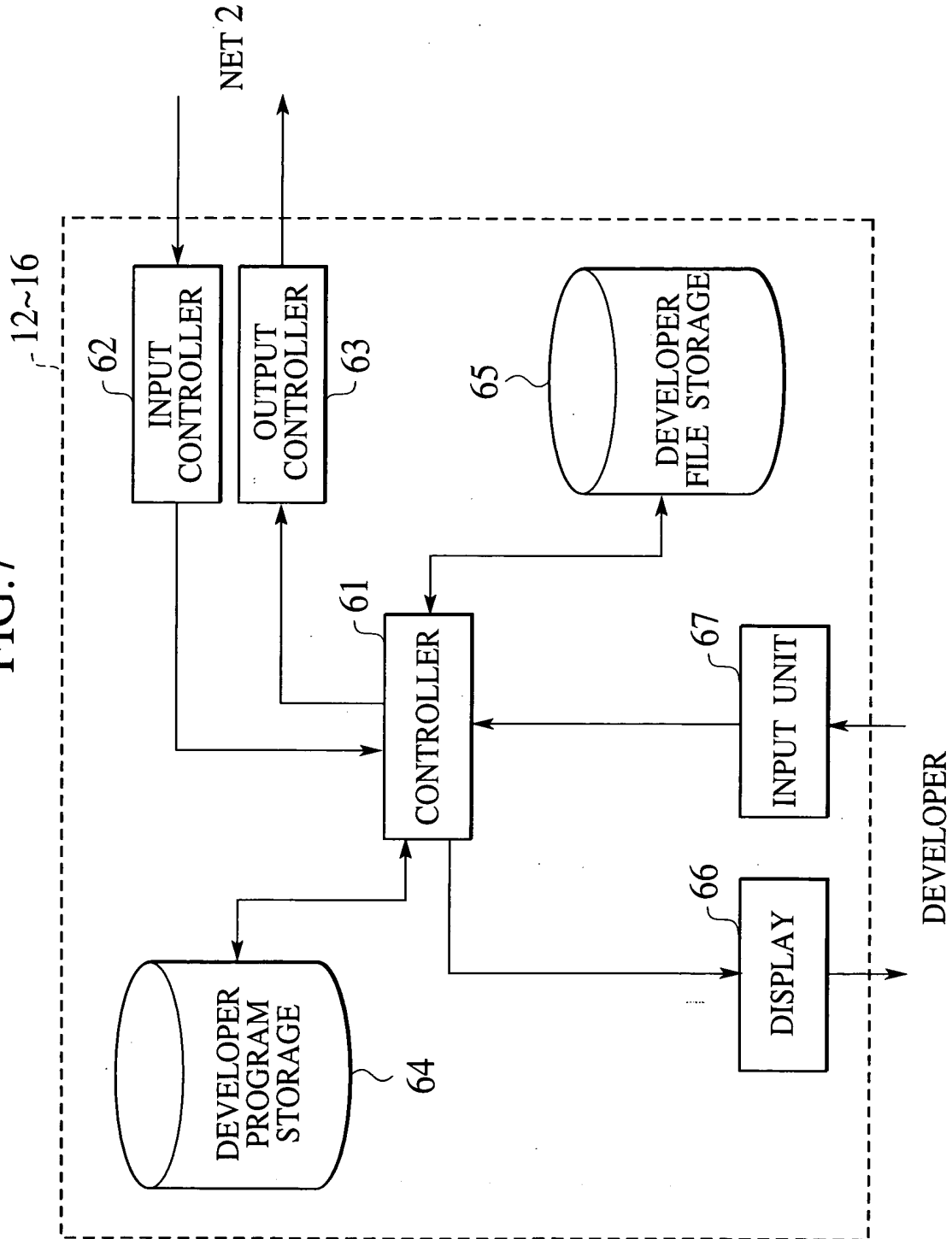
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FIG.6



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FIG. 7



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FIG.8

MAKER FILE 71

74 REGISTRATION NO.

75 MAKER NAME PROCESS NAME

76 CATEGORY FEATURES: CAPACITY, ACCURACY

OTHER CATEGORY MAKERS TO WHICH INTERFACING IS DESIRED

OTHER CATEGORY MAKERS ALREADY INTERFACED

TAT

PRICE

FIG.9

INTERFACE FILE 72

REGISTRATION NO.	MAKER NAME	TAT	PRICE	NUMBER
DESIGNING				
MASK MAKING				
Si				
PACKAGING				
TESTING				

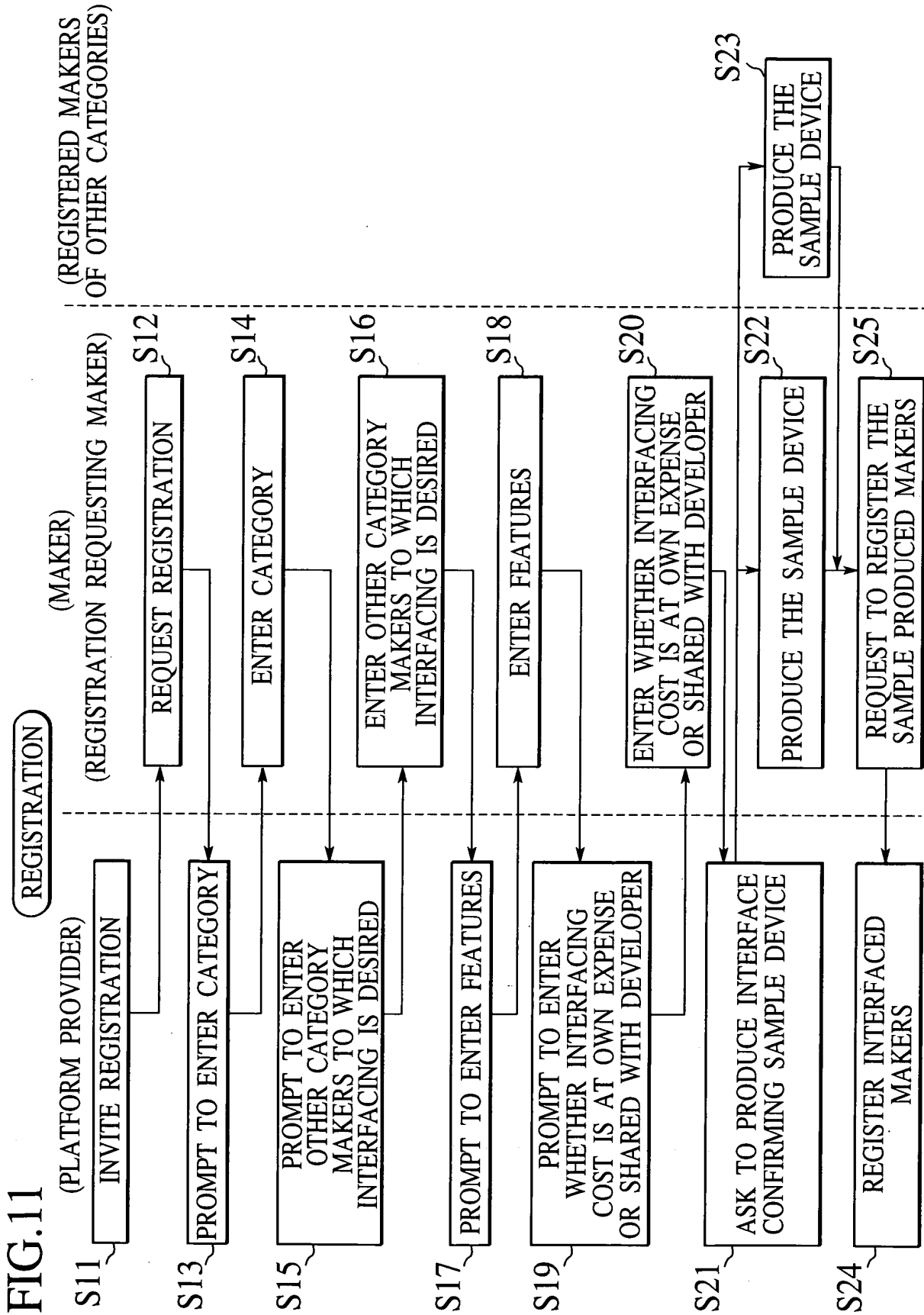
77 78 79 80 81

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FIG.10
DEVELOPER FILE 73

REGISTRATION NO.					
DEVELOPER NAME					
SPECIFICATIONS { • FUNCTION • DELIVERY TIME • PRICE					
RELATED MAKER NAMES AND TATS					
CATEGORY	DESIGNING	MASK MAKING	Si	PACKAGING	TESTING
MAKER NAME					
TAT					

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FIG.12

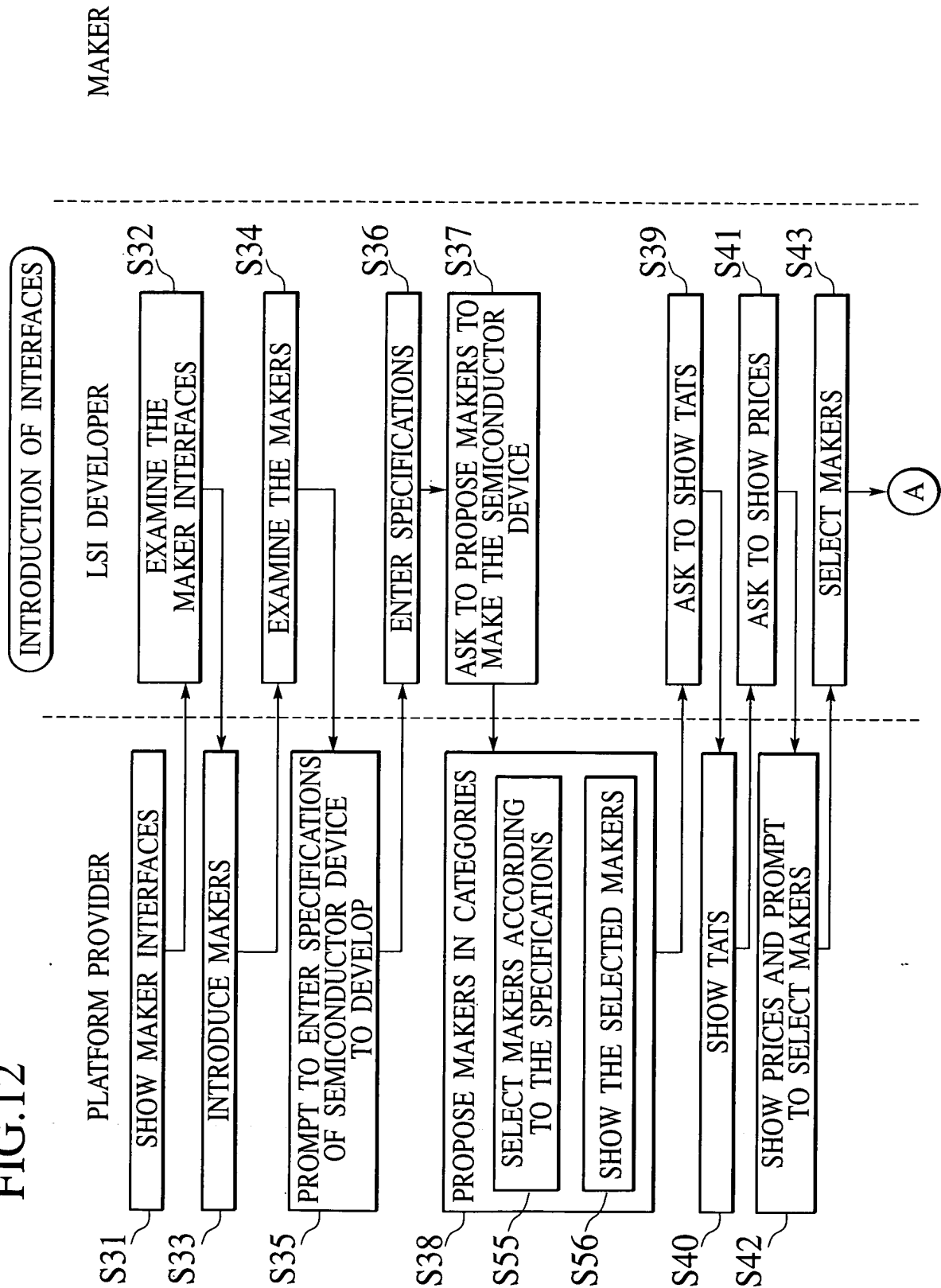
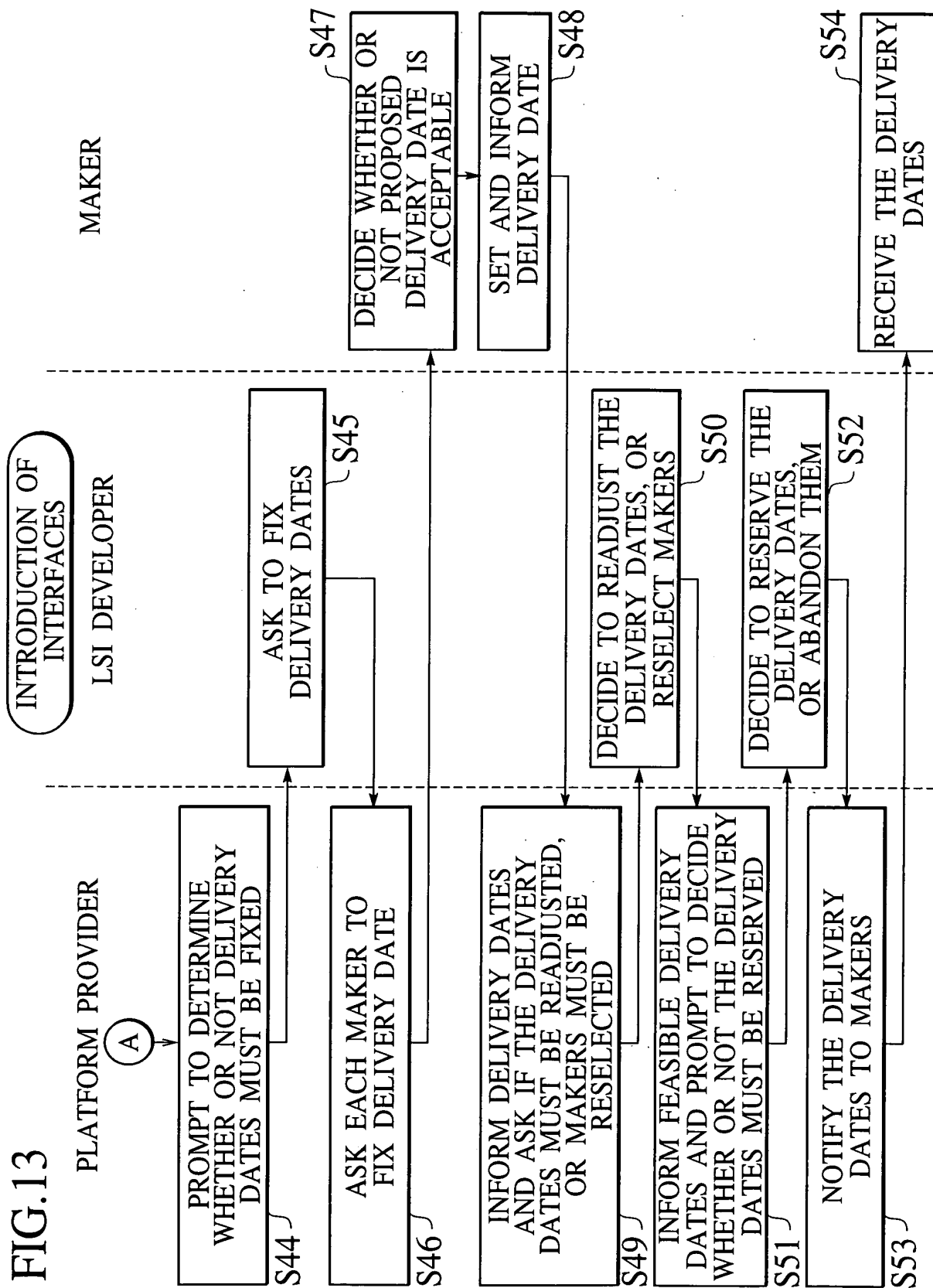


FIG. 13



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FIG.14

Document View

WELCOME TO ASIC DEVELOPING INTERFACES

WELCOME TO ASIC DEVELOPING INDUSTRIAL PARK
BEST SOLUTION FOR YOUR ASIC DEVELOPMENT

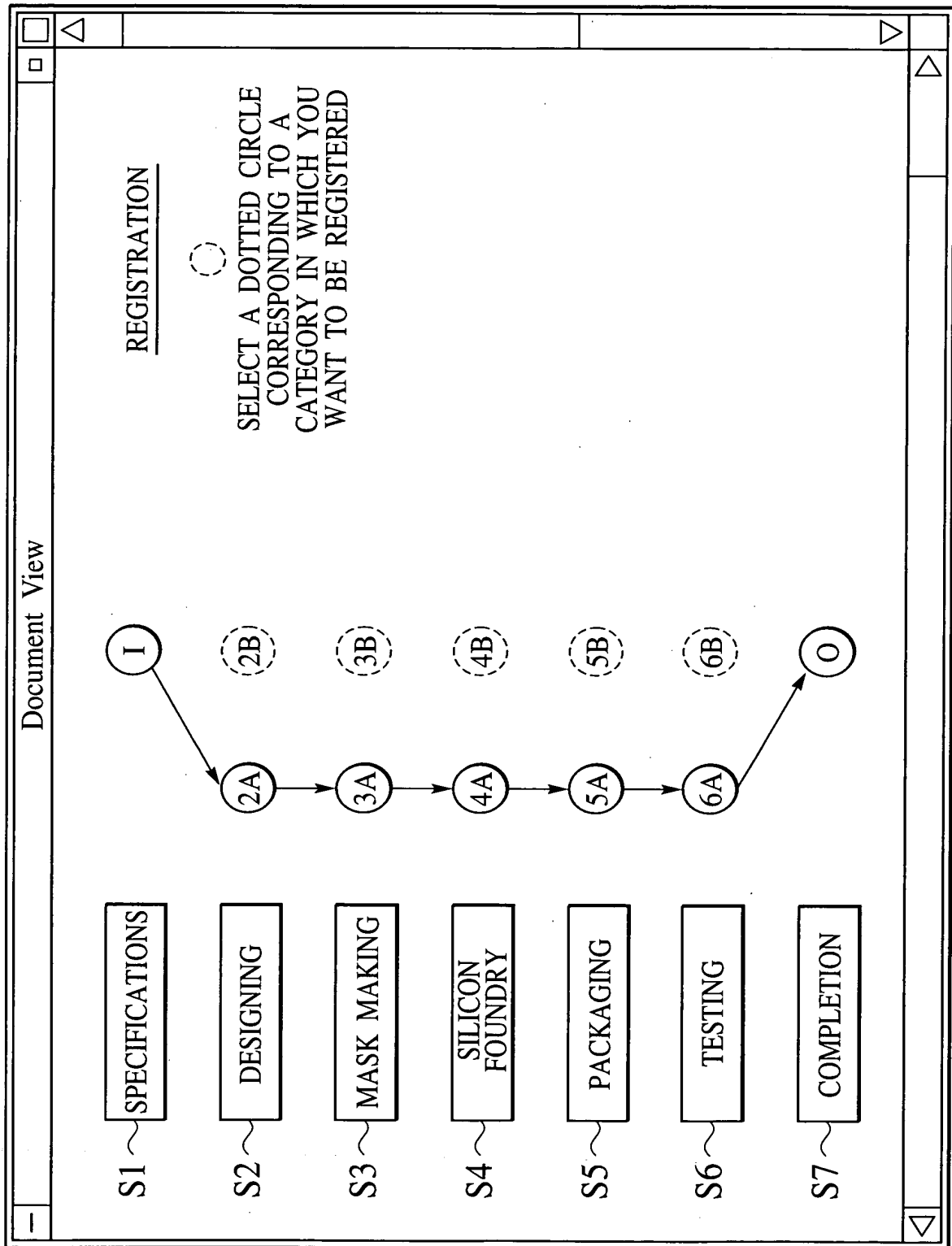
INTRODUCTION OF MAKERS
INTRODUCTION OF MAKER-TO-MAKER INTERFACES
YOUR SPECIFICATIONS AND OUR PROPOSAL

REGISTER IF YOU WANT AN INTERFACE
TO YOUR FACTORY

REGISTER

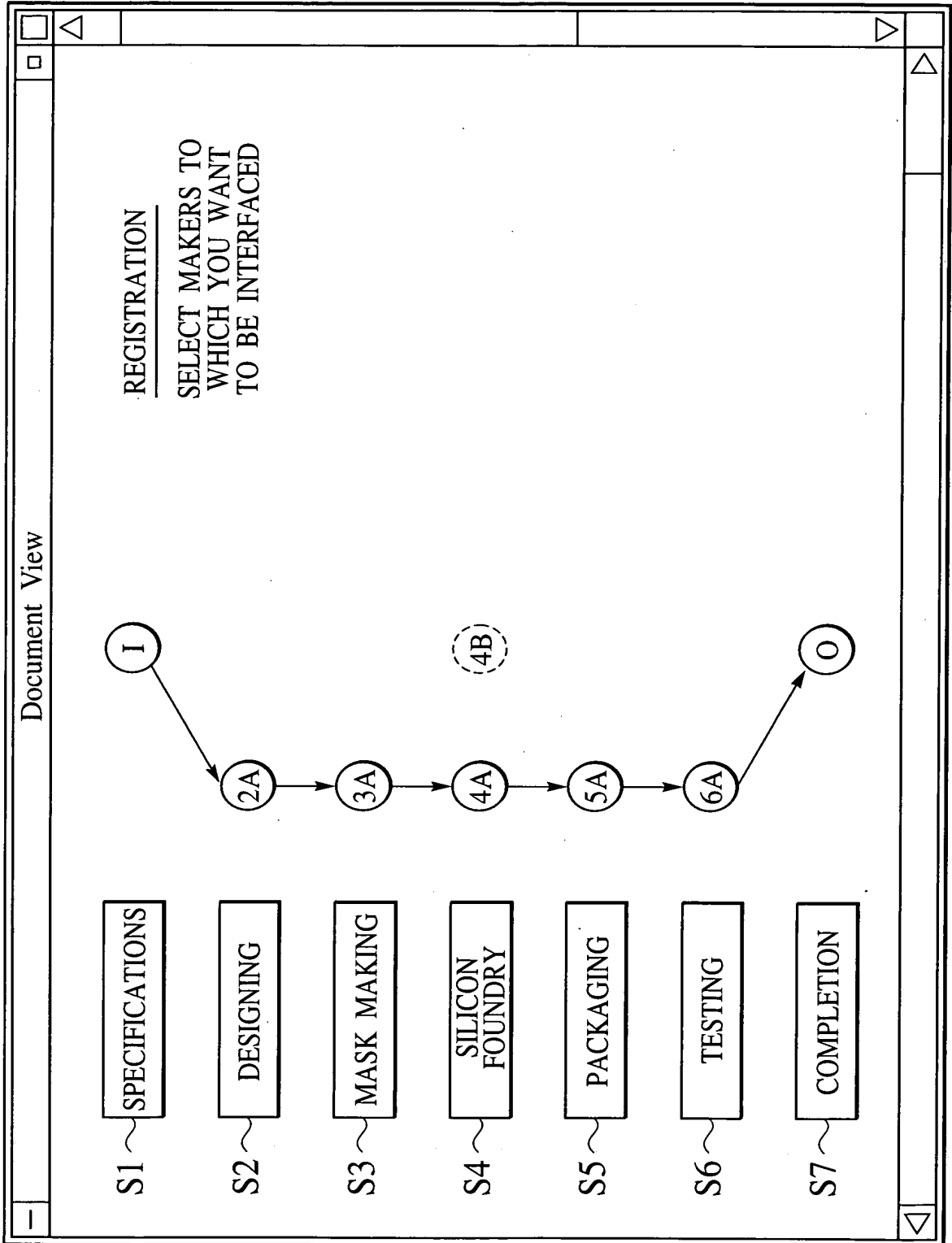
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FIG.15



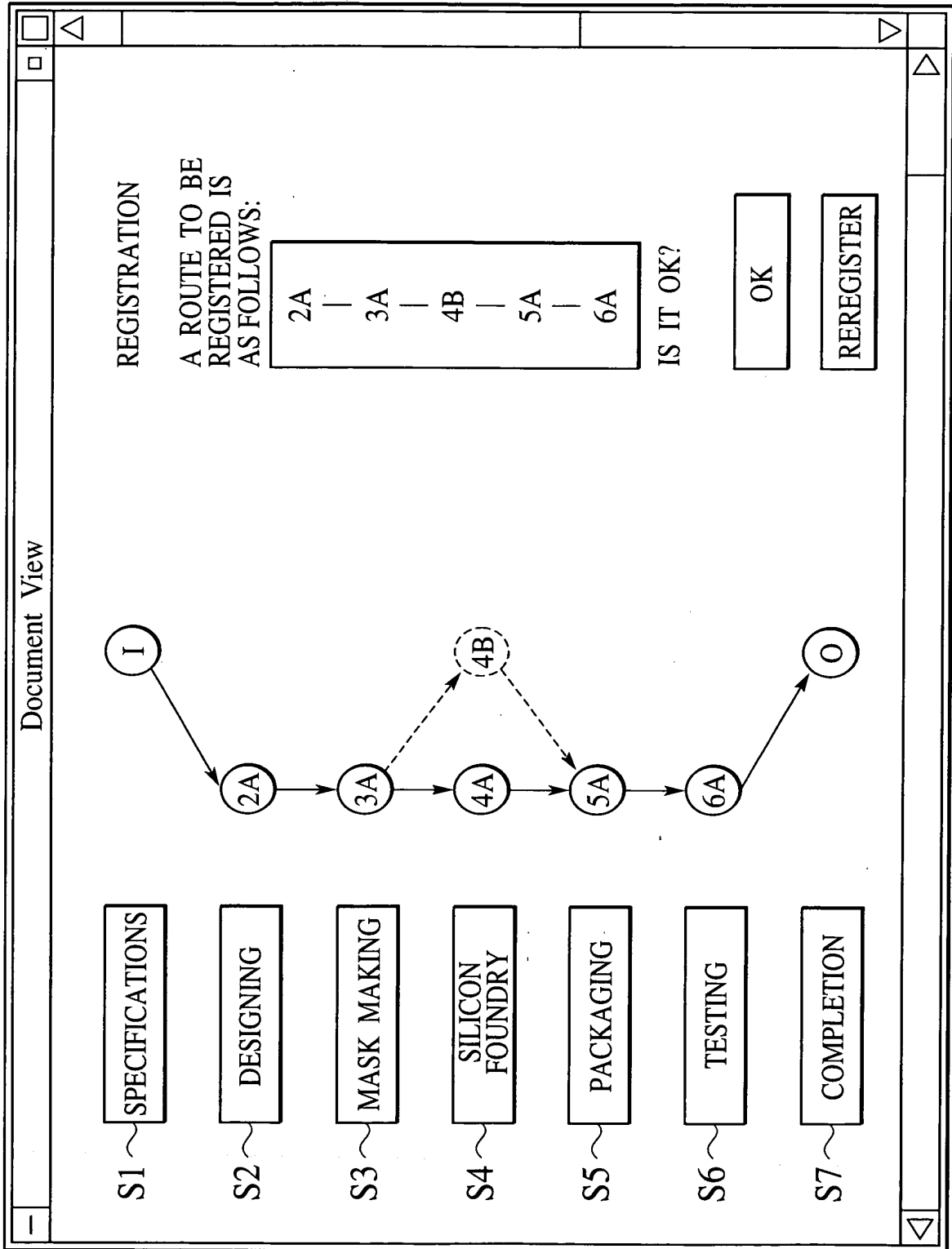
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FIG.16



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FIG.17



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FIG.18

Document View

FEATURE REGISTRATION

REGISTRATION NO. :

MAKER NAME :

CATEGORY :

TAT :

PRICE :

FEATURES :

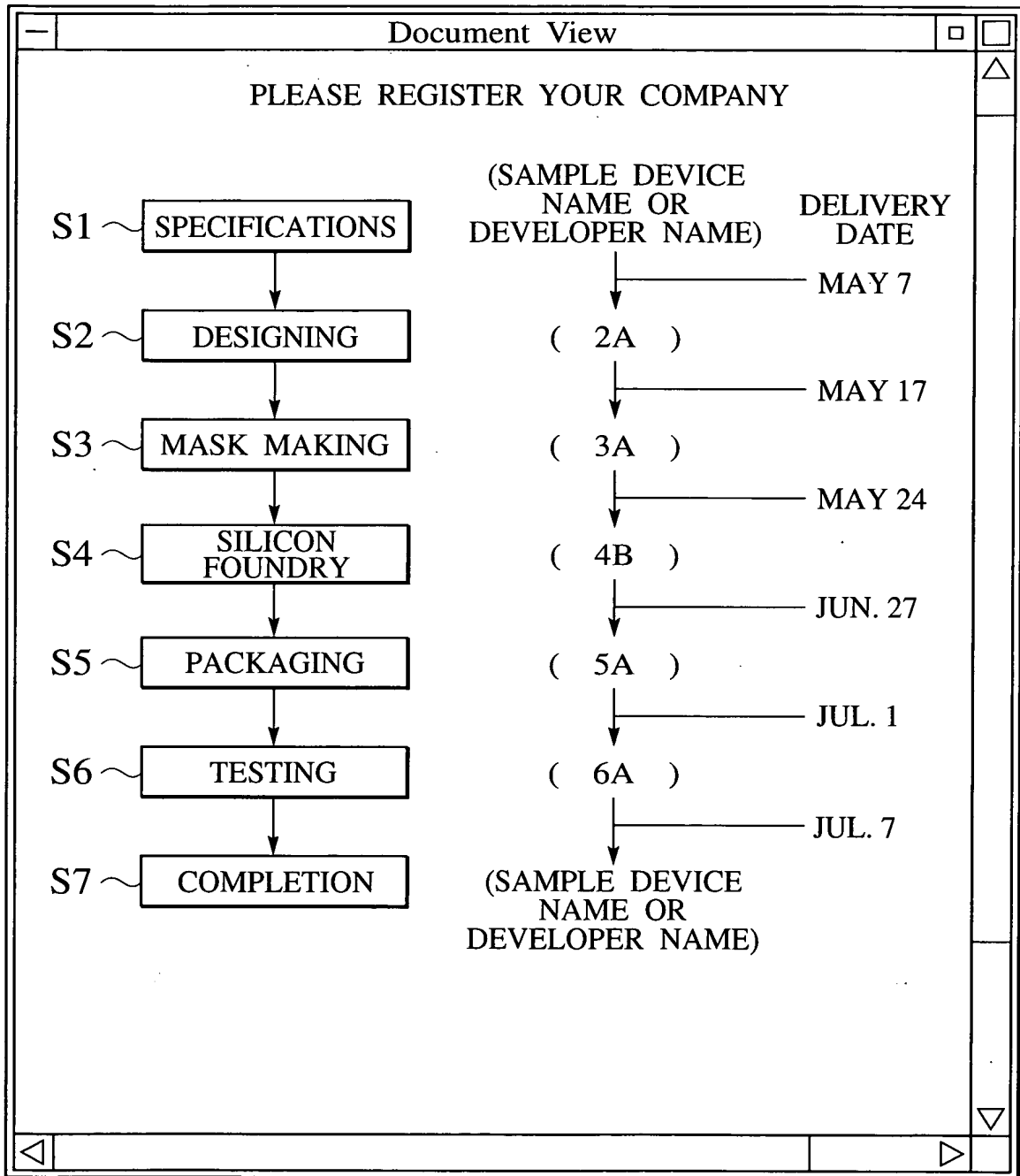
DESIRED INTERFACING ROUTE

☒ INTERFACING AT OWN COST

☐ INTERFACING AT SHARED
COST WITH DEVELOPER

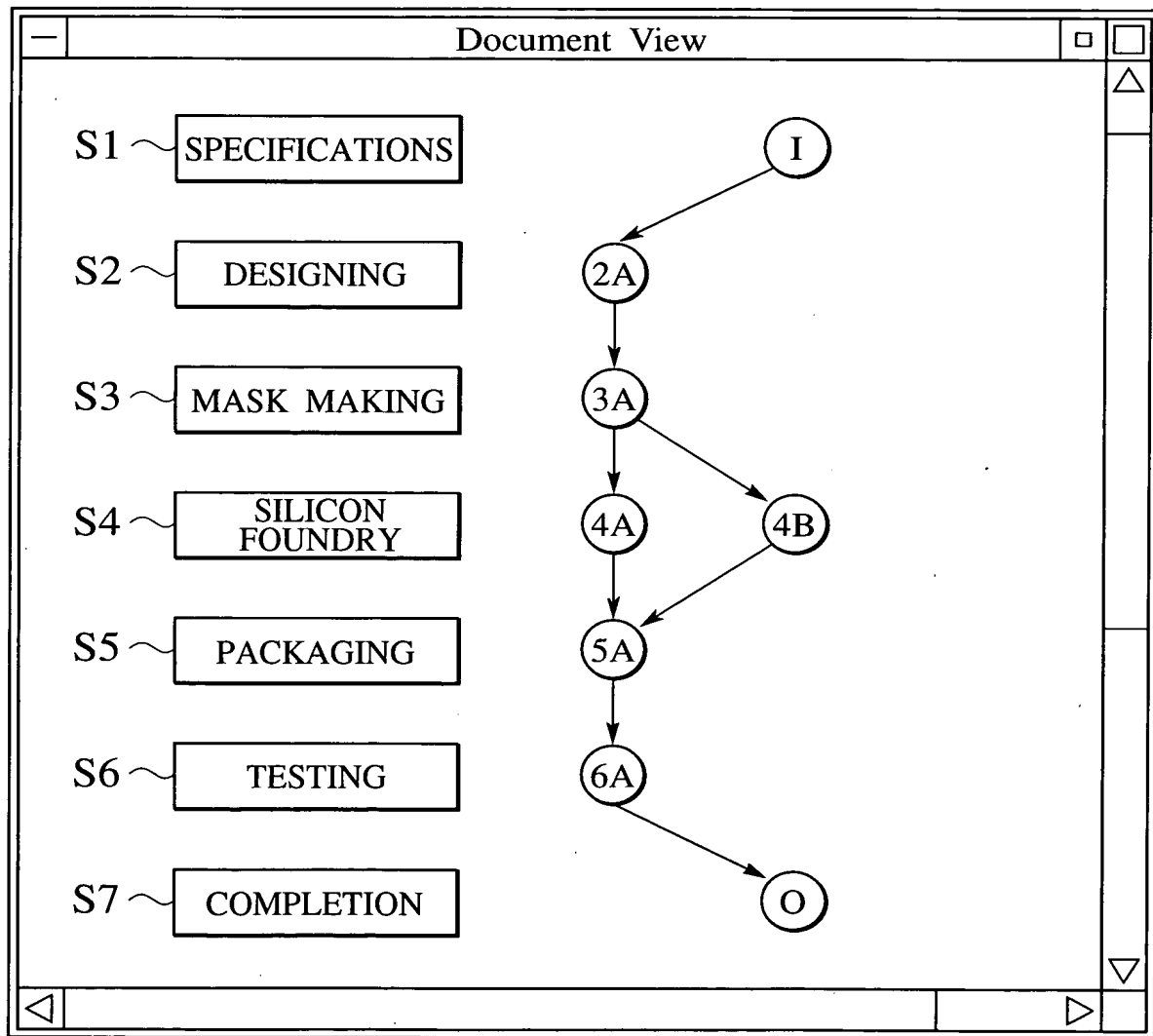
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FIG.19



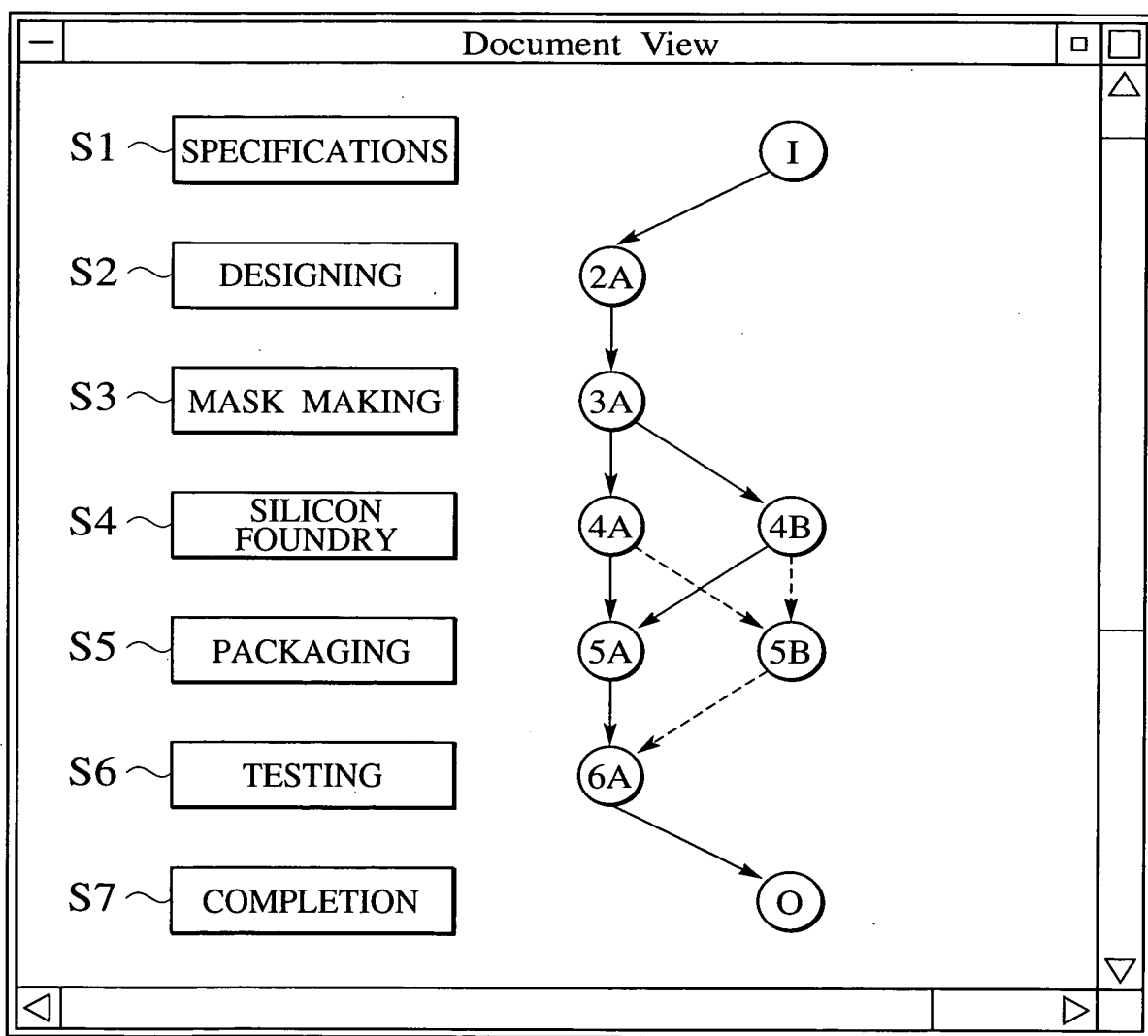
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FIG.20



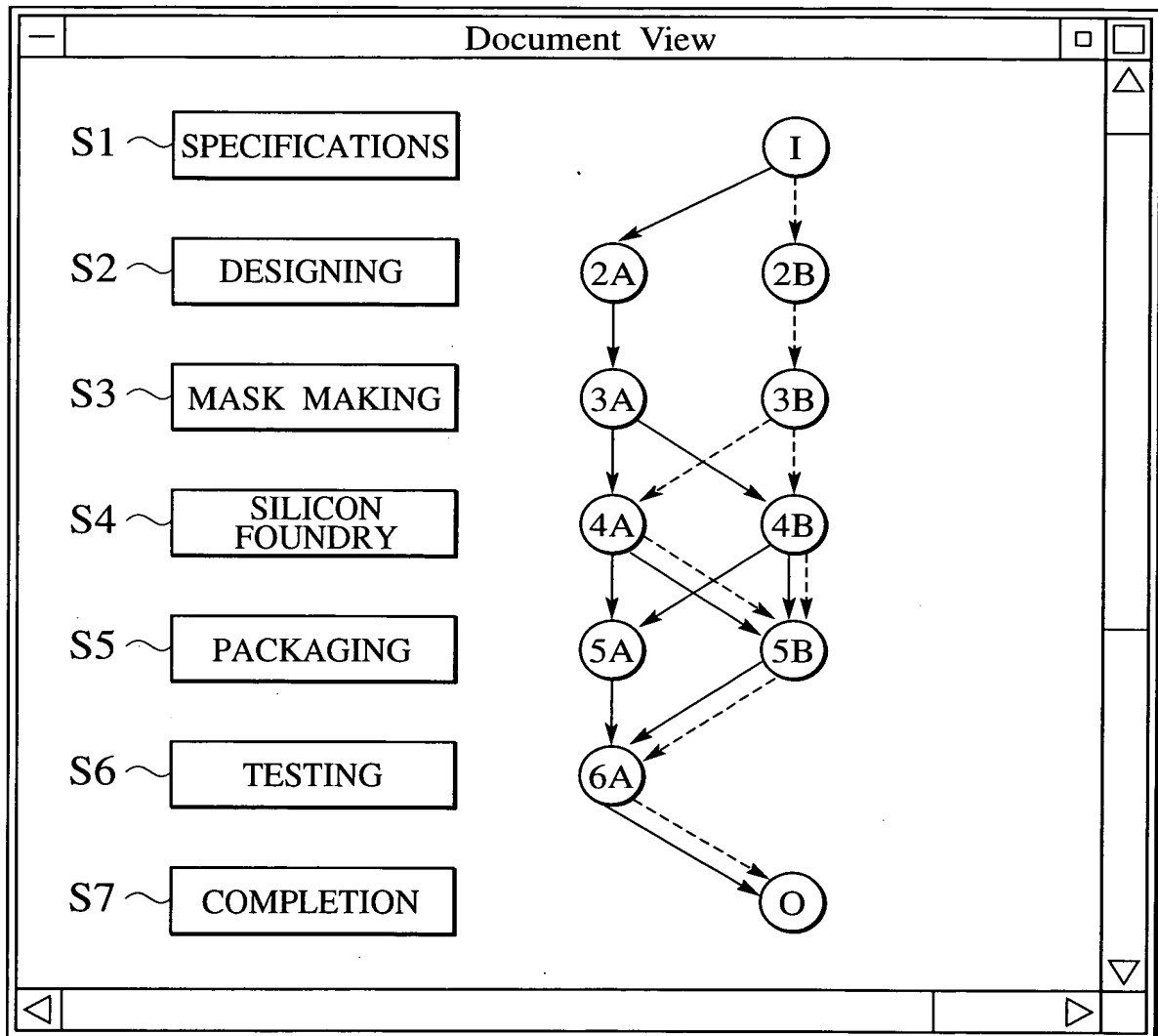
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FIG.21



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FIG.22



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FIG.23

Document View

INTRODUCTION OF MAKERS

MAKERS ARE CATEGORIZED.
SELECT A CATEGORY.

CATEGORY : 1. DESIGNING

: 2. MASK MAKING

: 3. SILICON FOUNDRY

: 4. PACKAGING

: 5. TESTING

KEYWORD SEARCH : ENTER KEYWORDS

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FIG.24

Document View

INTRODUCTION OF MAKER

REGISTRATION NO. :

CATEGORY :

MAKER NAME :

FEATURES :

TAT :

PRICE :

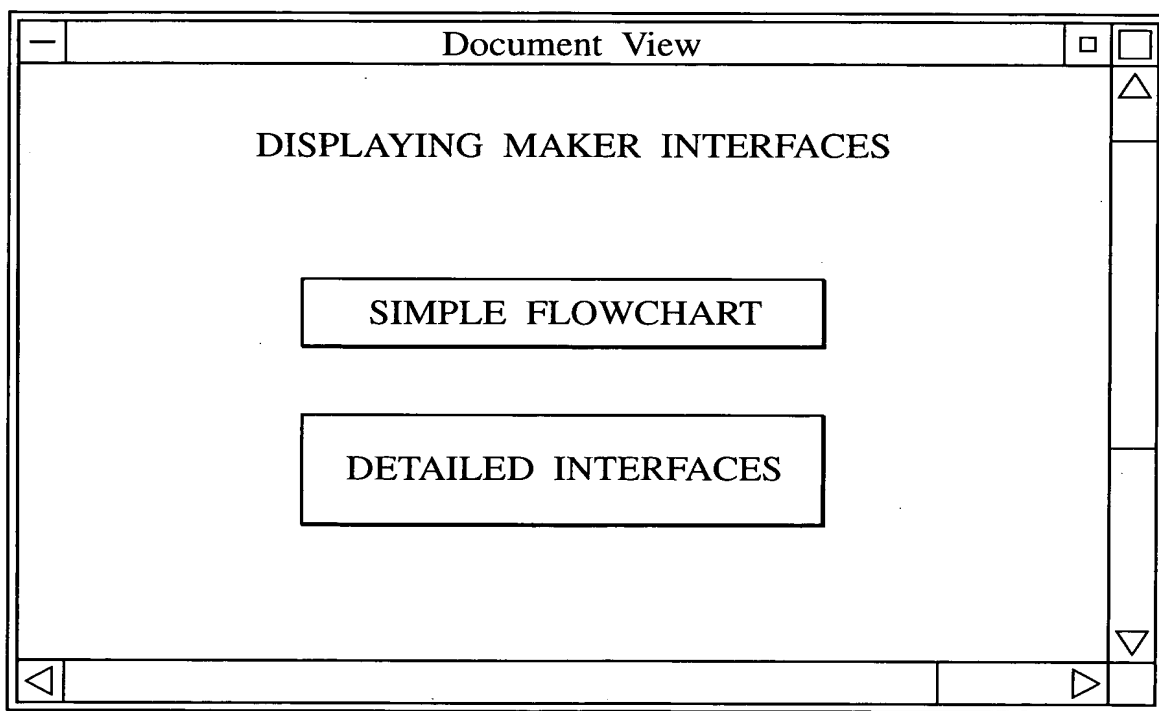
CONNECTABLE MAKERS IN THE PRECEDING CATEGORY

CONNECTABLE MAKERS IN THE SUCCEEDING CATEGORY

MAKERS TO BE INTERFACED IN THE FUTURE

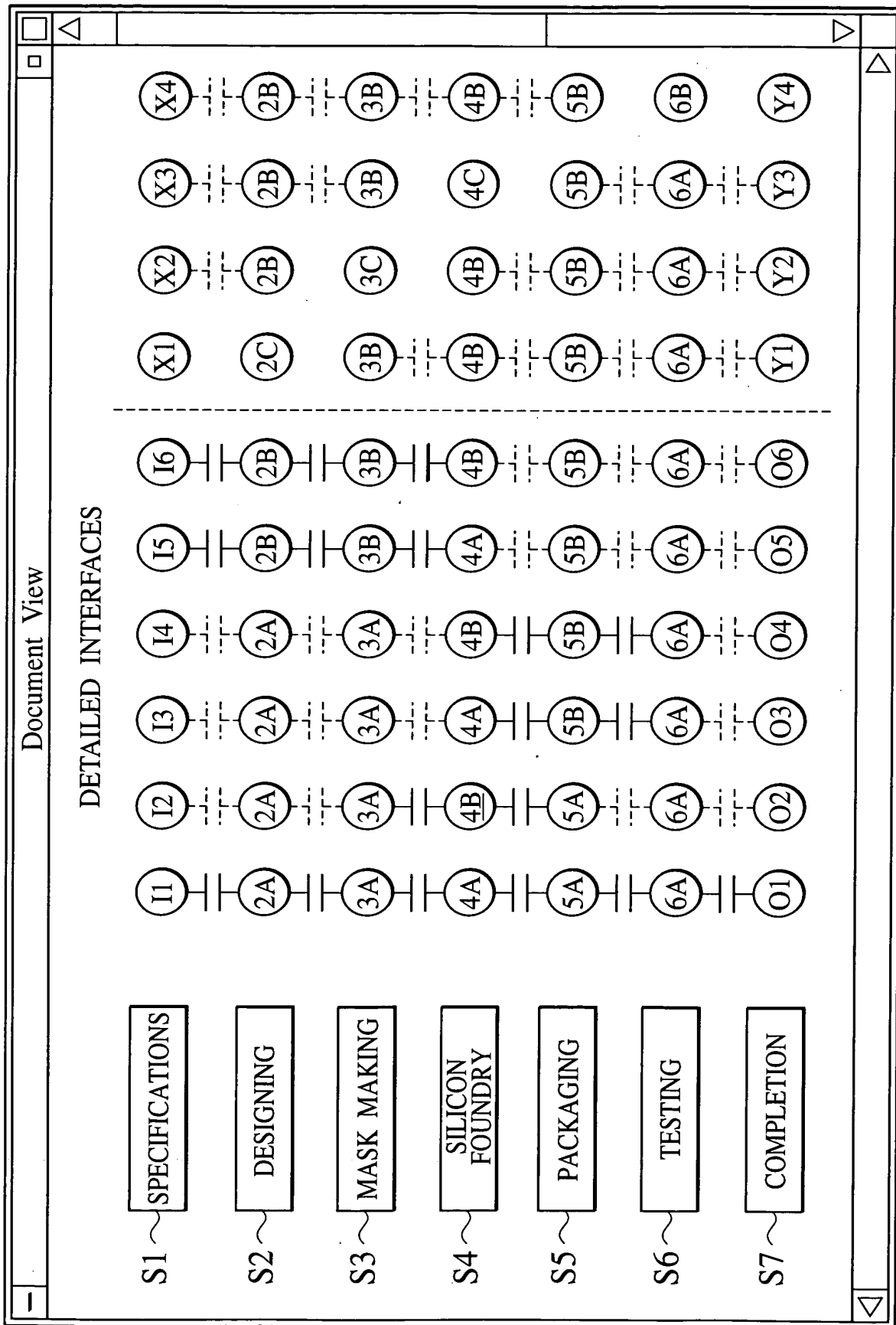
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FIG.25



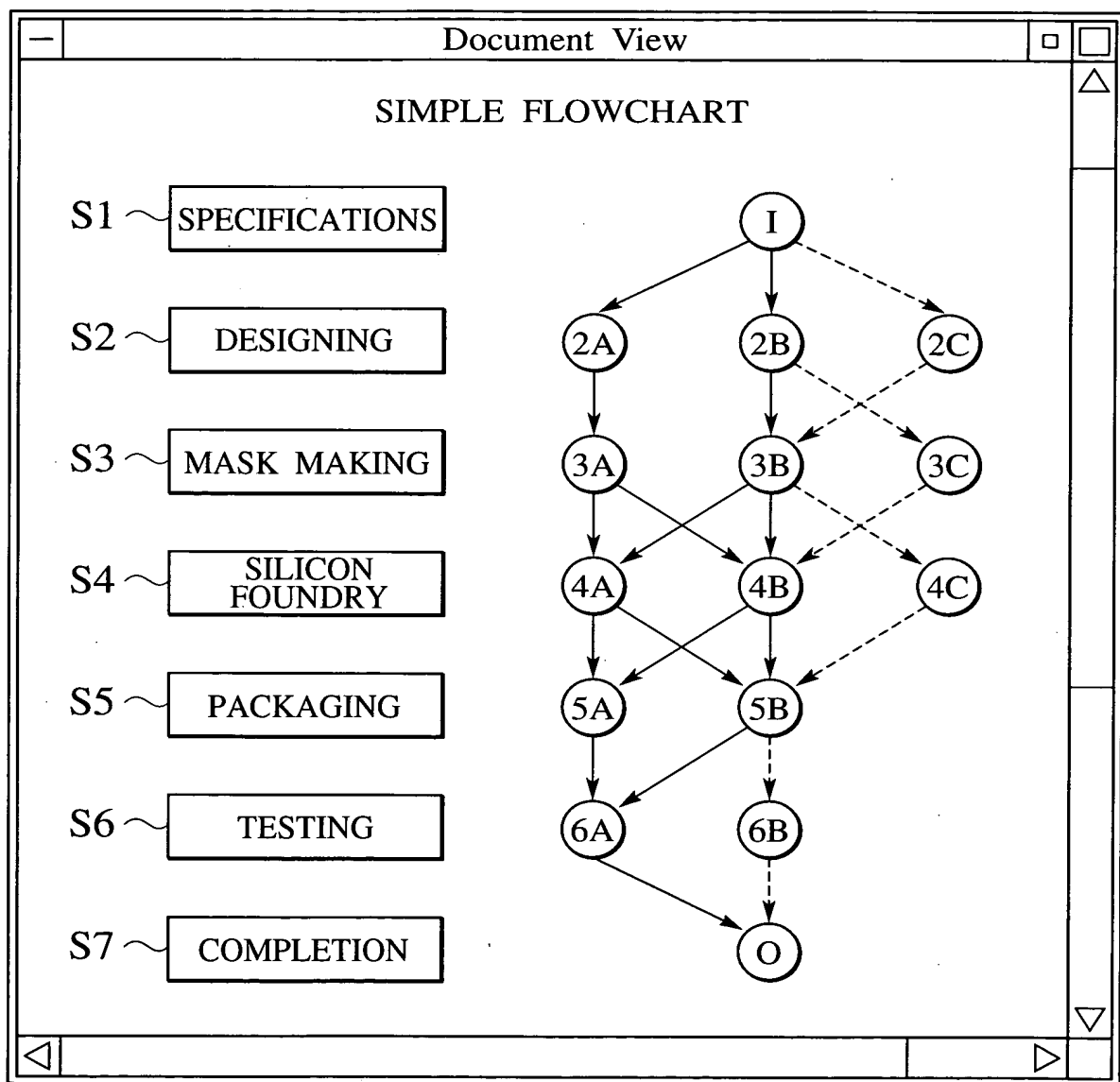
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FIG.26



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FIG.27



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FIG.28

Document View

ENTER SPECIFICATIONS OF ASIC TO DEVELOP

1. FUNCTION

: USE

CELLULAR PHONE

: OPERATIONAL CONDITIONS

W-CDMA COMPATIBLE, MPEG4 COMPATIBLE

: CIRCUIT SCALE

ONE MILLION GATES

2. DESIRED DELIVERY DATE

OCT.10

ESTIMATED ORDER DATE

AUG.10

3. REQUIRED NUMBER

1000 PIECES

UNIT PRICE

¥3,000

4. DESIRED MAKERS

1ST PREFERENCE

Si FOUNDRY

2ND PREFERENCE

3RD PREFERENCE

MAKER

4B

CLICK "PROPOSAL REQUEST" AFTER FILLING BLANKS.

PROPOSAL REQUEST

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FIG.29

Document View

PROPOSAL

1. PROPOSED FLOW (MAKER GROUPS)

1.	I2 - O2	<input type="checkbox"/>	5.	<input type="text"/>	<input type="checkbox"/>
2.	I4 - O4	<input type="checkbox"/>	6.	<input type="text"/>	<input type="checkbox"/>
3.	I6 - O6	<input checked="" type="checkbox"/>	7.	<input type="text"/>	<input type="checkbox"/>
4.	x2 - y2	<input type="checkbox"/>	8.	<input type="text"/>	<input type="checkbox"/>

CHECK A FLOW TO ADOPT.

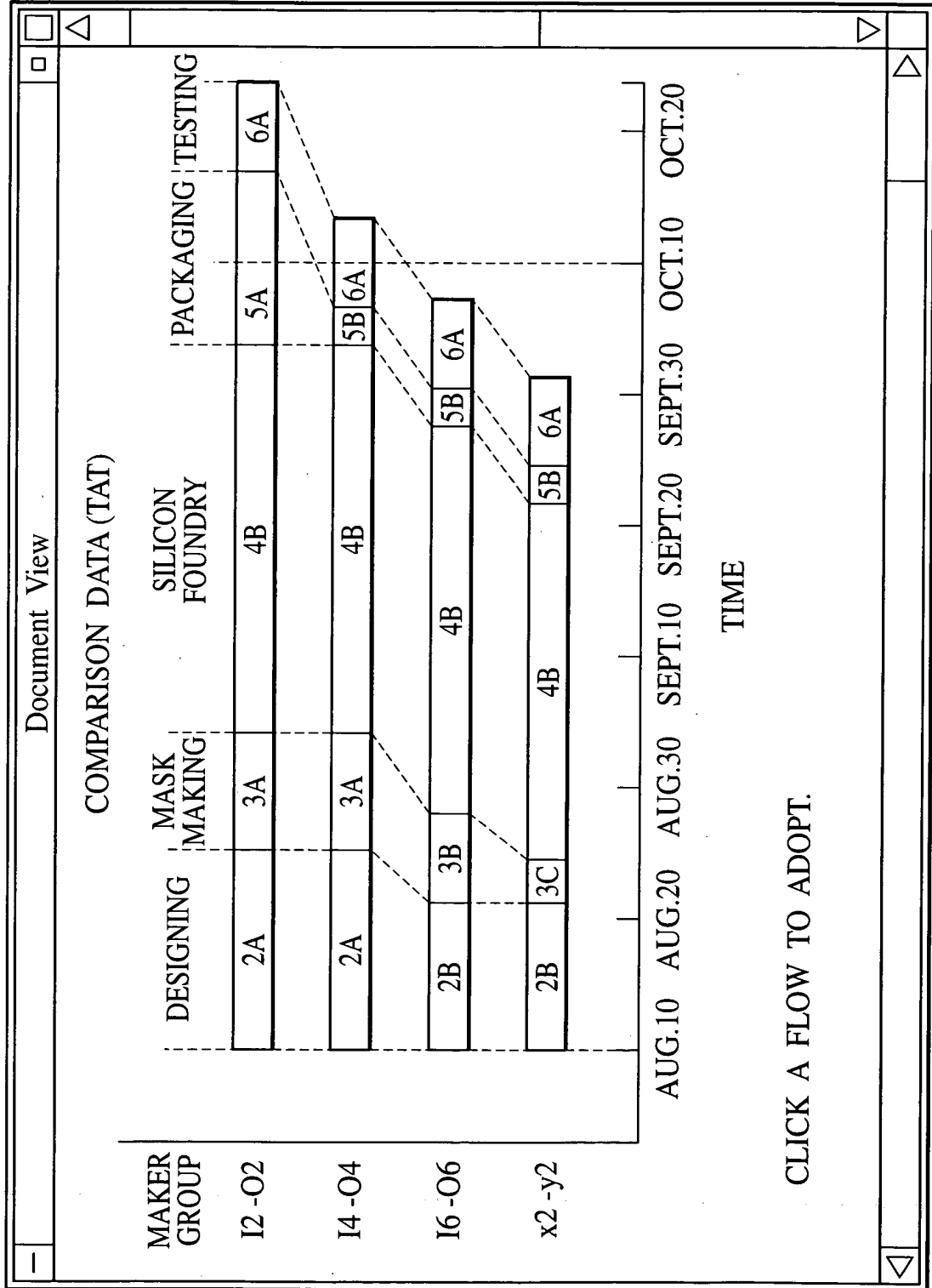
2. CLICK A DESIRED ITEM FOR COMPARISON DATA.

TAT

PRICE

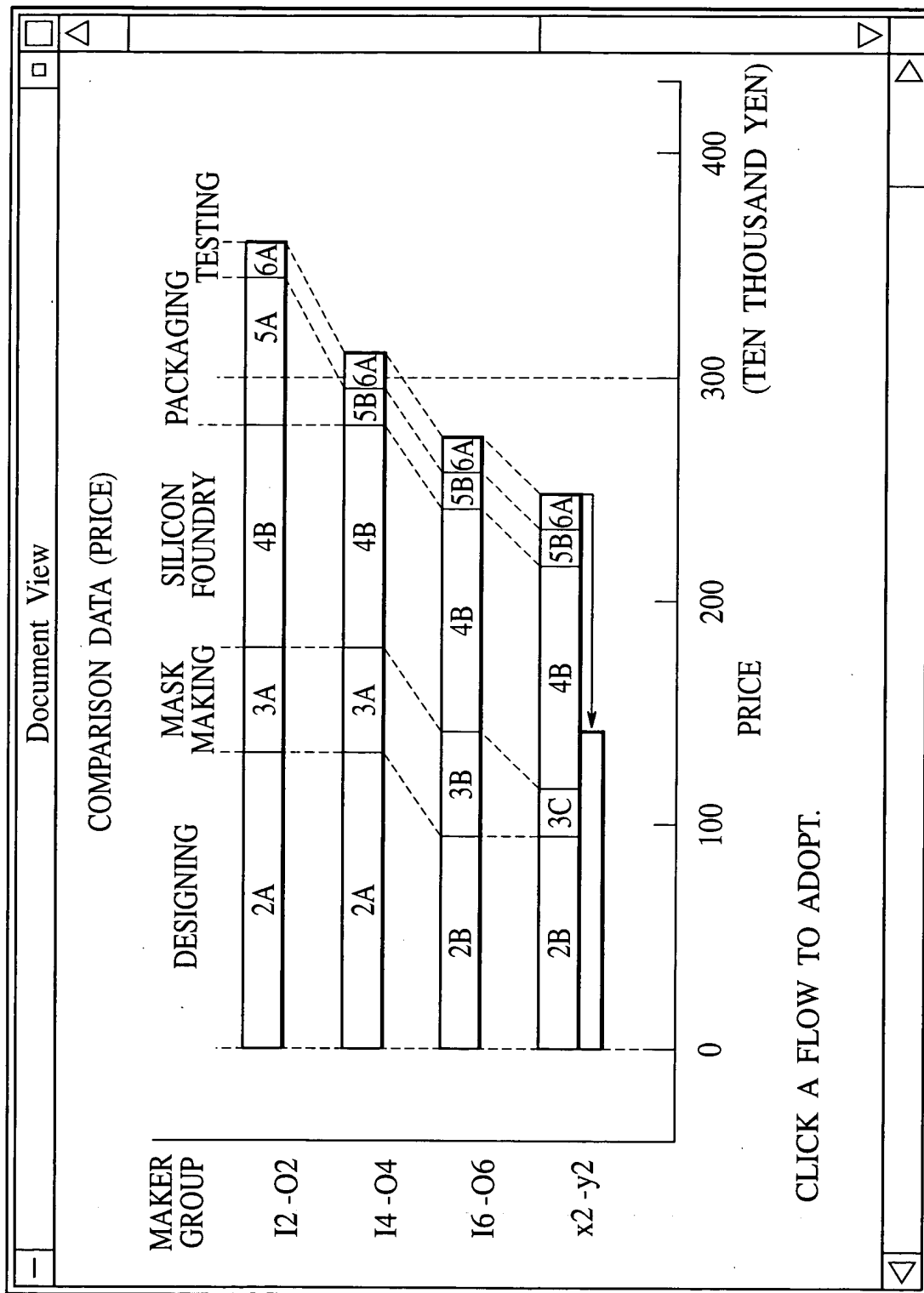
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FIG.30



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FIG.31



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FIG.32

Document View

(TENTATIVE) CONTRACT

YOU HAVE SELECTED I6-06

WILL YOU ADJUST DETAILED SCHEDULE WITH MAKERS ?

YES, NOW

NO

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FIG.33

Document View

SCHEDULING

TO

SCHEDULING REQUEST FROM PLATFORM PROVIDER

DEVELOPER NAME

DEVICE SPECIFICATIONS

NUMBER

PRICE

AMENDMENT

EXPECTED START DATE →

EXPECTED DELIVERY DATE →

MAKERS INVOLVED IN THE SCHEDULE

ARE THE ABOVE START AND DELIVERY DATES ACCEPTABLE?
IF ACCEPTABLE, CLICK
AND IF AMENDMENTS ARE NEEDED,
ENTER DESIRED DATES IN BLANKS UNDER "AMENDMENT."

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FIG.34

Document View

RESCHEDULING IS REQUESTED.

MAKER IS REQUESTING RESCHEDULING.

REQUEST OF

REQUESTED AMENDMENT

EXPECTED START DATE →

EXPECTED DELIVERY DATE →

DO YOU RESCHEDULE?

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FIG.35

